

**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
GROUP ART UNIT 2812**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor:	Katsuki Hazama	§	Atty.Dkt.No.:	5957-82403
Serial Number:	10/725,318	§	Examiner:	Richard A. Booth
Filing Date:	December 2, 2003	§	Group/Art Unit:	2812
Title:	Semiconductor Device Including Transistor with Composite Gate Structure and Transistor with Single Gate Structure, and Method for Manufacturing the Same	§ § § § § § § §	Conf. No.	2698

**RESPONSE TO FINAL OFFICE ACTION MAILED JUNE 6, 2007**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to a Final Office Action of June 6, 2007, to further highlight why the application is in condition for allowance.